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CONFIRMATION NO. 6791

SERIAL NUMBER 10/517,765	FILING OR 371(c) DATE 02/03/2006 RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. 040509
APPLICANTS Takashi Ozaki, Nakano-ku, JAPAN; Kenichi Suzuki, Nakono-ku, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/08097 06/26/2003				
** FOREIGN APPLICATIONS ***** JAPAN 2002-187566 06/27/2002				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 18
Verified and Acknowledged Examiner's Signature _____ Initials _____			INDEPENDENT CLAIMS 6	
ADDRESS 23850				
TITLE Substrate treating apparatus and method for manufacturing semiconductor device				
FILING FEE RECEIVED 1630	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	